

S/N 09/510,375

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brett L. Williams

Examiner: Hong Kim

Serial No.: 09/510,375

Group Art Unit: 2185

Filed: February 22, 2000

Docket: 303.164US3

Title: SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A
BURST EXTENDED DATA OUT MODE



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2100

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant further request that a copy of the 1449 form, initialled by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Under 37 C.F.R. §1.97(b)(3), it is believed that no fee or certificate is required with this Information Disclosure Statement.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

BRETT L. WILLIAMS

By his Representatives,

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6/25/2001

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Date of Deposit: June 25, 2001

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Box CPA, Washington, D.C. 20231.